

APPLICATION

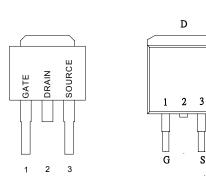
- Buck Converter High Side Switch
- Other Applications

V _{DSS}	R _{DS(ON)} Typ.	I _D	
30V	10.8mΩ	50A	•

PIN CONFIGURATION

TO-252 Front View

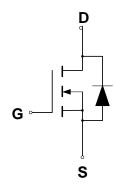
TO-263 Front View



FEATURES

- Low ON Resistance
- Low Gate Charge
- Peak Current vs Pulse Width Curve
- Inductive Switching Curves
- Improved UIS Ruggedness

SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Source Voltage (Note 1)	V _{DSS}	30	V
Drain to Current $-$ Continuous Tc = 25 $^{\circ}$ C, V _{GS} @10V (Note 2)	I _D	50	А
$-$ Continuous Tc = 100 $^\circ C$, V _{GS} @10V (Note 2)	Ι _D	Fig.3	
$-$ Pulsed Tc = 25 $^{\circ}$ C, V _{GS} @10V (Note 3)	I _{DM}	Fig.6	
Gate-to-Source Voltage – Continue	V _{GS}	±20	V
Total Power Dissipation		52	W
Derating Factor above 25 $^\circ\!\mathrm{C}$		0.5	W/° C
Peak Diode Recovery dv/dt (Note 4)	dv/dt	3.0	V/ns
Operating Junction and Storage Temperature Range		-55 to 150	°C
Single Pulse Avalanche Energy L=1.1mH,I _D =30 Amps		500	mJ
Maximum Lead Temperature for Soldering Purposes		300	°C
Maximum Package Body for 10 seconds	T _{PKG}	260	°C
Pulsed Avalanche Rating		Fig.8	

THERMAL RESISTANCE

Symbol	Parameter	Min	Тур	Max	Units	Test Conditions
$R_{ extsf{ heta}JC}$	Junction-to-case			2.4	°C/W	Water cooled heatsink, P_D adjusted for a peak junction temperature of +150 $^\circ\!\mathrm{C}$
$R_{ extsf{ heta}JA}$	Junction-to-ambient (PCB Mount)			50	°C/W	Minimum pad area, 2-oz copper, FR-4 circuit board, double sided
$R_{\theta JA}$	Junction-to-ambient			62	°C/W	1 cubic foot chamber, free air



ORDERING INFORMATION

Part Number	Package
CMT60N03GN252	TO-252
CMT60N03GN263	TO-263

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, T_J = 25 $^\circ\!\mathrm{C}$.

				CMT60N030	3	
Cha	racteristic	Symbol	Min	Тур	Max	Units
	OFF Characterist	ics				
Drain-to-Source Breakdown Voltage		V _{DSS}	30			V
$(V_{GS} = 0 V, I_D = 250 \mu A)$						
Breakdown Voltage Temperature Coe	$\Delta V_{DSS} / \Delta T_{J}$		27		mV/°C	
(Reference to 25°C, I_D = 250 μ A)						
Drain-to-Source Leakage Current		I _{DSS}				μA
$(V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 25^{\circ}C)$					1	
$(V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125^{\circ}C)$					10	
Gate-to-Source Forward Leakage		I _{GSS}			100	nA
(V _{GS} = 20 V)						
Gate-to-Source Reverse Leakage		I _{GSS}			-100	nA
(V _{GS} = -20 V)						
	ON Characteristi	cs				1
Gate Threshold Voltage, Fig. 12		$V_{GS(th)}$	1.0		3.0	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-to-Source On-Resistance	e, Fig.9,10 (Note 5)	R _{DS(on)}				mΩ
$(V_{GS} = 10 \text{ V}, I_{D} = 15\text{A})$				10.8	12.5	
(V _{GS} = 4.5 V, I _D = 12A)				15.4		
Forward Transconductance (V _{DS} = 15	V, $I_D = 12A$) (Note 5)	g _{FS}		28		S
	Dynamic Character	istics		1	1	1
Input Capacitance	$(V_{DS} = 15 V, V_{GS} = 0 V,$	C _{iss}		1520		pF
Output Capacitance	f = 1.0 MHz)	C _{oss}		314		pF
Reverse Transfer Capacitance	Fig.14	C _{rss}		152		pF
Total Gate Charge (V _{GS} = 10 V)		Qg		27.9	35	nC
Total Gate Charge (V_{GS} = 4.5 V)	$(V_{DS} = 15 \text{ V}, I_{D} = 12 \text{ A}) \text{ (Note 6)}$	Qg		14	19	nC
Gate-to-Source Charge	Fig.15	Q _{gs}		4.9		nC
Gate-to-Drain Charge		Q_gd		4.3		nC
	Resistive Switching Char	acteristics				
Turn-On Delay Time		t _{d(on)}		10		ns
Rise Time	$(V_{DD} = 15 \text{ V}, \text{ I}_{D} = 12 \text{ A}, V_{GS} = 10 \text{ V}.$	tr		3.4		ns
Turn-Off Delay Time	$v_{GS} = 10 V$, R _G = 1.0 Ω) (Note 6)	$t_{d(off)}$		36		ns
Fall Time	$R_G = 1.0\Omega$ (Note 6)	t _f		6.0		ns
Turn-On Delay Time		t _{d(on)}		16		ns
Rise Time	$(V_{DD} = 15 V, I_D = 12 A,$	tr		7.2		ns
Turn-Off Delay Time	V _{GS} = 4.5V, R _G = 1.0Ω) (Note 6)	t _{d(off)}		34		ns
Fall Time	$R_G = 1.0\Omega$ (Note 6)	t _f		14		ns
	Source-Drain Diode Char	acteristics				
Continuous Source Current (Body		Is			50	А
Diode Fig.16)	Integral pn-diode in MOSFET					
Pulse Source Current (Body Diode)		I _{SM}			Fig.6	А
Forward On-Voltage	(I _S = 12 A, V _{GS} = 0 V)	V _{SD}			1.0	V
Forward Turn-On Time	$(I_F = 12 \text{ A}, V_{GS} = 0 \text{ V},$	t _{rr}		25	38	ns
Reverse Recovery Charge	$d_i/d_t = 100A/\mu s)$	Q _{rr}		31	46	nC

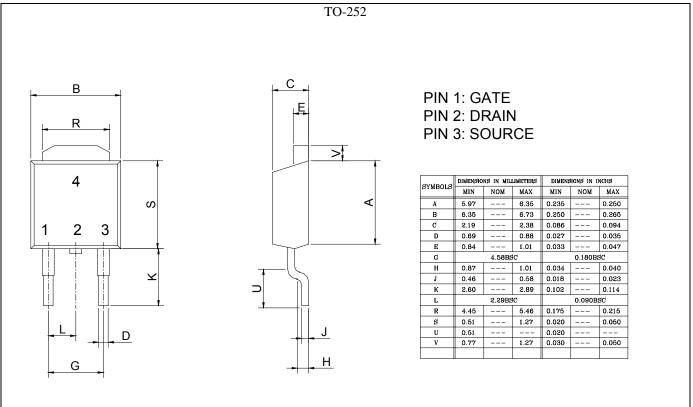


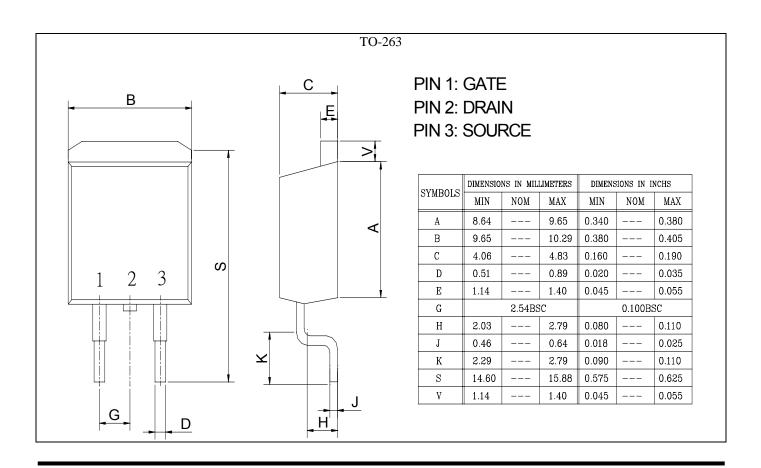
Note 1: T_J = +25°C to 150°C

- Note 2: Current is calculated based upon maximum allowable junction temperature. Package current limitation is 30A.
- Note 3: Repetitive rating; pulse width limited by maximum junction temperature.
- Note 4: I_{SD} = 12.0A, di/dt ≤100A/µs, V_{DD} ≤ BV_{DSS}, T_J = +150°C
- Note 5: Pulse width \leq 250µs; duty cycle \leq 2%
- Note 6: Essentially independent of operating temerpature.



PACKAGE DIMENSION







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